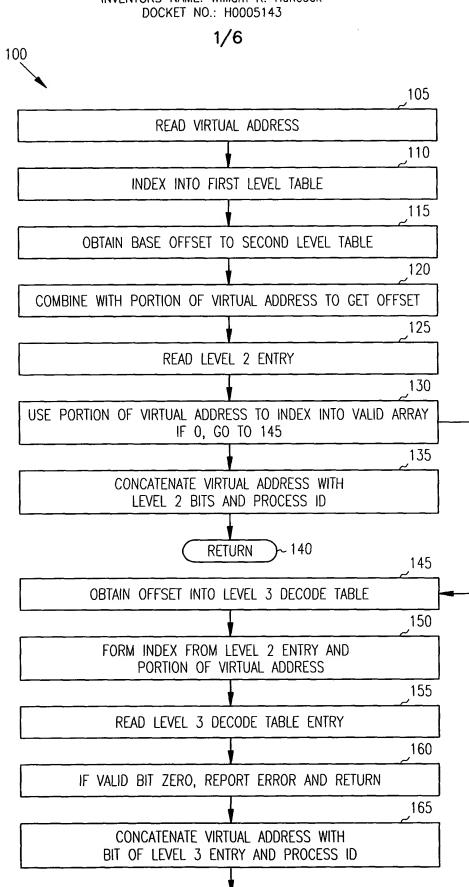
TITLE: VIRTUAL MEMORY TRANSLATOR FOR REAL-TIME OPERATING SYSTEMS
INVENTORS NAME: William R. Hancock
DOCKET NO: HO005143

• ,...



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FIG. 1

TITLE: VIRTUAL MEMORY TRANSLATOR FOR REAL-TIME OPERATING SYSTEMS INVENTORS NAME: William R. Hancock DOCKET NO.: H0005143

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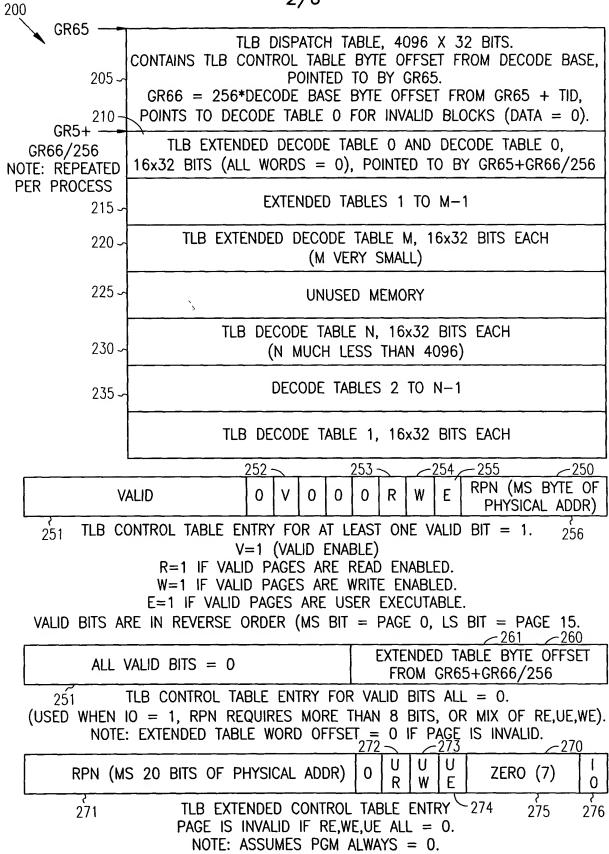


FIG. 2

TITLE: VIRTUAL MEMORY TRANSLATOR FOR REAL—TIME OPERATING SYSTEMS INVENTORS NAME: William R. Hancock DOCKET NO.: H0005143

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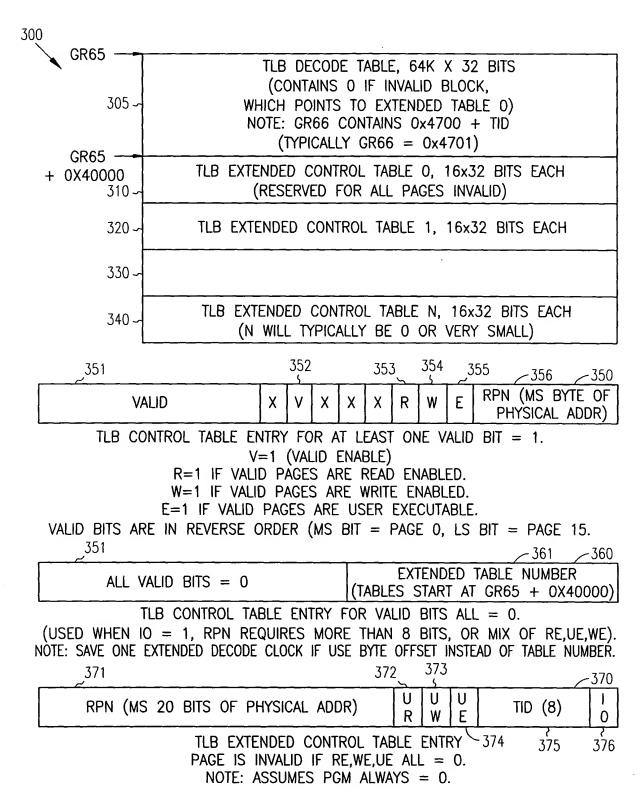
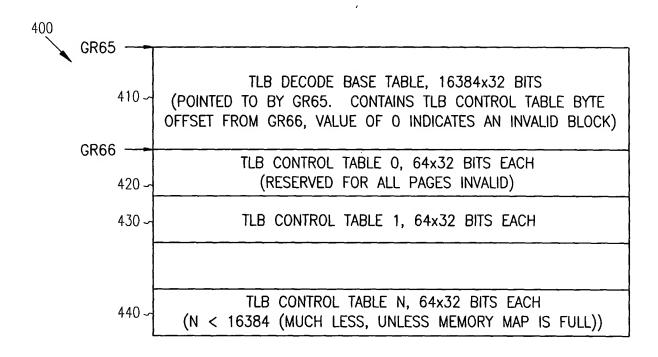


FIG. 3

TITLE: VIRTUAL MEMORY TRANSLATOR FOR REAL-TIME OPERATING SYSTEMS INVENTORS NAME: William R. Hancock

DOCKET NO.: H0005143

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PHYSICAL ADDR (20)	U R	W C	U E	TID (8)	10
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TLB CONTROL TABLE EXTRY

UR = 1 IF PAGE IS READ ENABLED.

UW=1 IF PAGE IS WRITE ENABLED.

UE=1 IF PAGE IS USER EXECUTABLE.

IO = 1 IF PAGE IS IN I/O SPACE.

PAGE IS INVALID IF RE,WE,UE ALL = 0.

NOTE: ASSUMES PGM = 0.

FIG. 4

TITLE: VIRTUAL MEMORY TRANSLATOR FOR REAL-TIME OPERATING SYSTEMS INVENTORS NAME: William R. Hancock DOCKET NO.: H0005143

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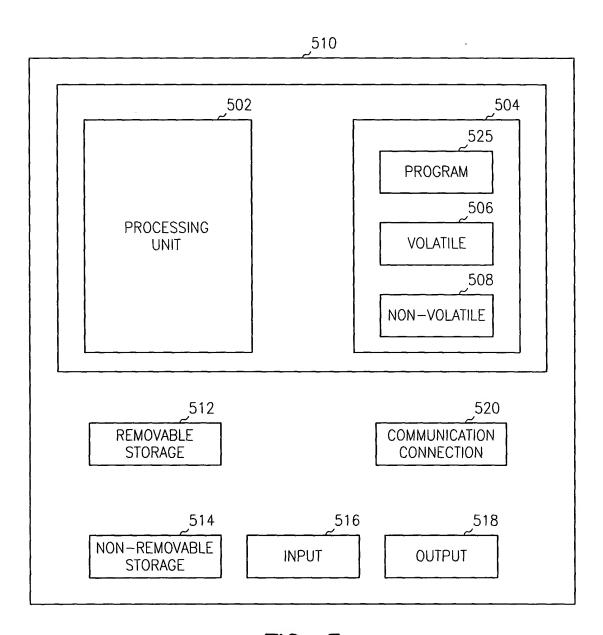


FIG. 5

TITLE: VIRTUAL MEMORY TRANSLATOR FOR REAL-TIME OPERATING SYSTEMS INVENTORS NAME: William R. Hancock

DOCKET NO.: H0005143

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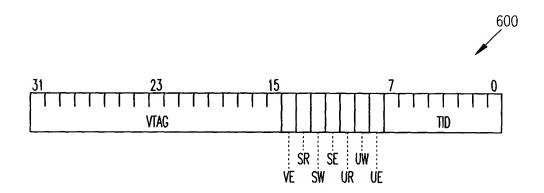


FIG. 6

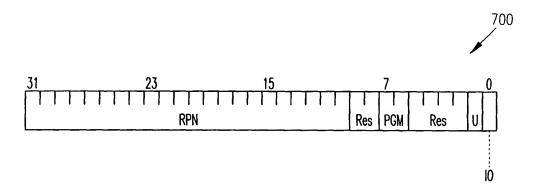


FIG. 7